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STATEMENT UNDER 37 CFR 3.73(b) Atty. Docket No. 3222.197000											
Applicant/	/Patent Owner: _(Garg et al.									
		6,044,4									
Titled:	Titled: RISC MICROPROCESSOR ARCHITECTURE IMPLEMENTING MULTIPLE TYPED REGISTER SETS										
Inte	ellectual Ventu	re Funding LLC	. a		Limi	ited Liability Company					
(Name of Ass	Intellectual Venture Funding LLC , a Limited Liability Company (Name of Assignee) (Type of Assignee, e.g., corporation, partnership, university, government agency, etc.										
states that it is:											
1. X	the assignee of the	he entire right, title, and	d interest in;								
2.	an assignee of less than the entire right, title, and interest in (The extent (by percentage) of its ownership interest is										
3.	the assignee of ε	an undivided interest in	the entirety of	(a complete	e assignm	nent from one of the joint inventors was made)					
the patent application/patent identified above, by virtue of either:											
A	An assignment from the inventor(s) of the patent application/patent identified above. The assignment was recorded in the United States Patent and Trademark Office at Reel, Frame, or for which a copy therefore is attached.										
OR	copy alordio.d.	attached.									
B. X	A chain of title fro	om the inventor(s), of the	ie patent applic	:ation/pater	nt identifie	ed above, to the current assignee as follows:					
	1. From:	Garg e	<u>t al.</u>		To:	S-MOS Systems, Inc.					
		ocument was recorded i									
	Reel .	014788	, Frame	0462		or for which a copy thereof is attached.					
	2. From:	2. From: S-MOS Systems, Inc. To: Seiko Epson Corporation									
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	3. From:	Seiko Epson C	orporation		To:	Transmeta Corporation					
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X	Additional docur	ments in the chain of titl	le are listed on	a supplem	ental shee	et(s)					
X Ası	required by 37 CF concurrently is being	R 3.73(b)(1)(i), the doc	cumentary evidentation pursuant	ence of the	e chain of	title from the original owner to the assignee was					
or concurrently is being, submitted for recordation pursuant to 37 CFR 3.11. [NOTE: A separate copy (<i>i.e.</i> , a true copy of the original assignment document(s)) must be submitted to Assignment Division in accordance with 37 CFR Part 3, to record the assignment in the records of the USPTO. See MPEP 302.08]											
The undersigned (whose title is supplied below) is authorized to act on behalf of the assignee.											
Signature											
Dona	ald J. Featherst	one, Registration N	Jo. 33.876			Authorized Person					
	inted or Typed Nar	Title									

This collection of information is required by 37 CFR 3.73(b). The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Statement Under 37 CFR 3.73(b) (PTO/SB/96) Supplemental Sheet for Chain of Title, Item B

Applic	cant/Owner:	Garg et al.								
Applic	cation No./Pat	ent No.:	6,044,449							
			March 28, 2000							
Title: RISC MICROPROCESSOR ARCHITECTURE IMPLEMENTING MULTIPLE TYPED REGISTER SETS										
Name	of Assignee:	Intellect	ual Venture Fundi	ng LLC						
			Liability Company							
			ocuments in the Ch							
4.	From:	Transı	neta Corporation	To:	Transmeta LLC					
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